

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: C. OKAMOTO et al
Serial No. 09/857,271
Filed: November 27, 2001

Group Art Unit: 2876
Examiner: D. Lee
For: METHOD OF CHECKING AUTHENTICITY OF
SHEET WITH BUILT-IN ELECTRONIC
CIRCUIT CHIP

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 30, 2003

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 09/857,271.

The Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,



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Date: September 30, 2003

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. ASA-1005-02		SERIAL NO.		
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT C. OKAMOTO et al				
				FILING DATE 09/30/03		GROUP 2876		
U.S. PATENT DOCUMENTS								
* EXAMINER INITIAL		DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE (If Appropriate)	
	AA	5,601,931	02/11/97	Hoshino et al				
	AB	5,754,654	05/1998	Hiroya et al				
	AC	5,629,981	05/1997	Nerlikar				
	AD	6,268,796	07/2001	Gnadinger et al				
	AE	5,748,737	05/1998	Daggar				
	AF							
	AG							
	AH							
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	AJ							
	AK							
FOREIGN PATENT DOCUMENTS								
		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL	57-161993	10/05/82	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AM	7-85172	03/31/95	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AN	4-248695	09/04/92	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AO	3-188590	08/16/91	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AP	10-291391	11/04/98	Japan			<input type="checkbox"/>	<input type="checkbox"/>
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)								
	AR							
	AS							
	AT							
EXAMINER				DATE CONSIDERED				